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12/07/2021

1) Initial release

02/16/2022

1) Update to onboard JLINK solution

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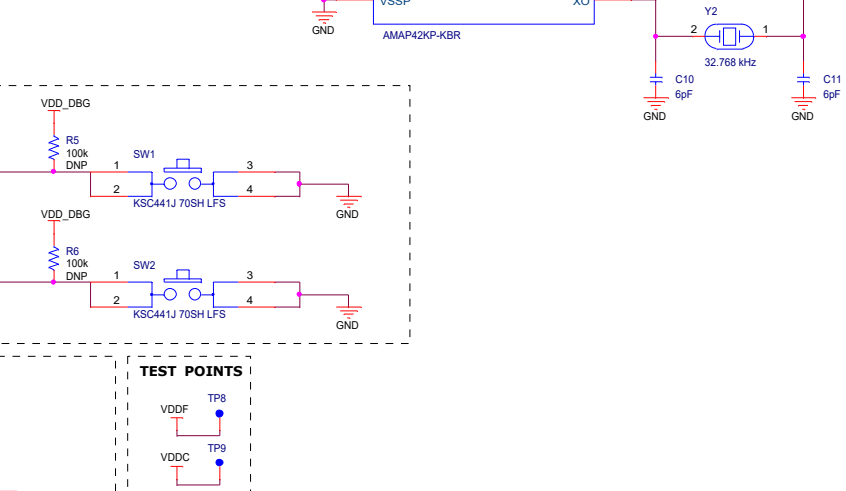
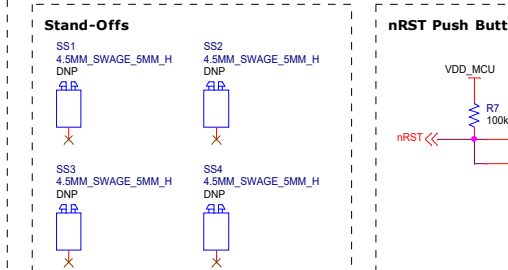
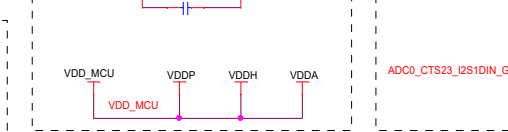
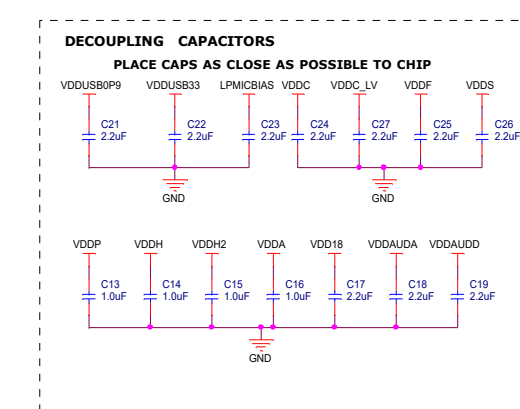
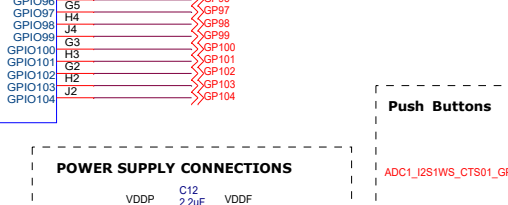
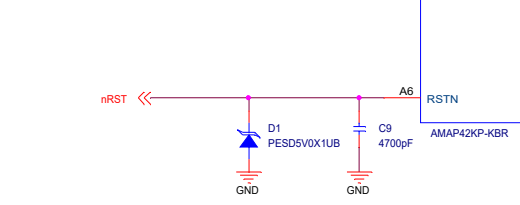
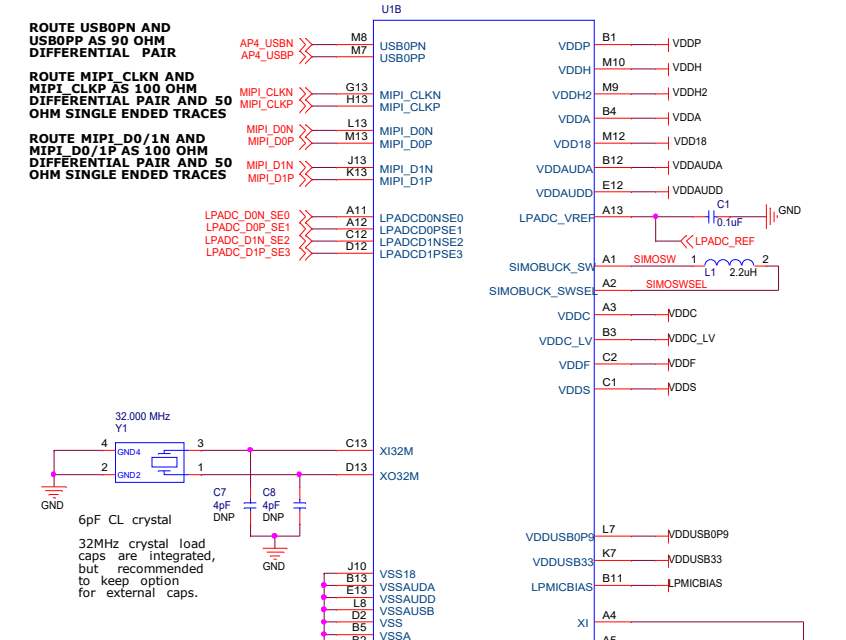
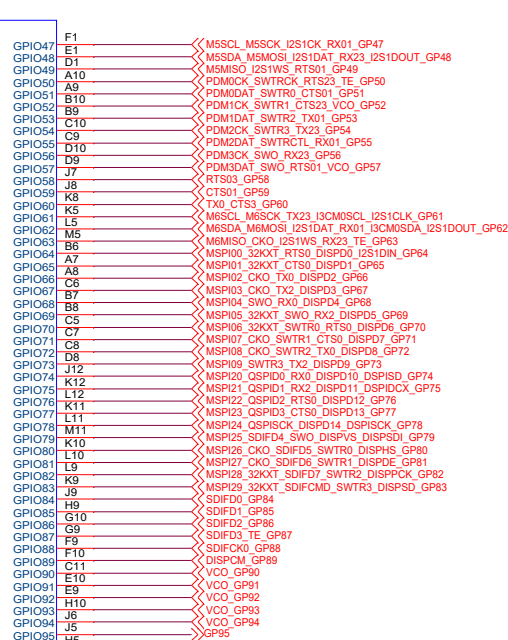
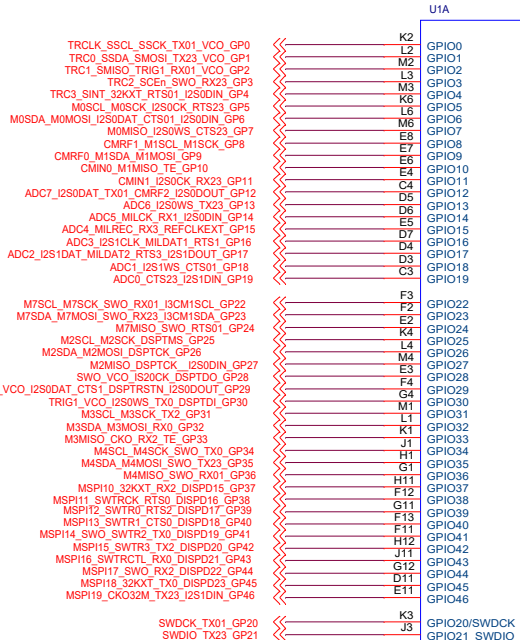
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P01: Revision History

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P02: Apollo 4 Plus MCU

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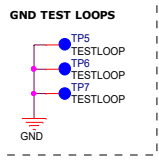
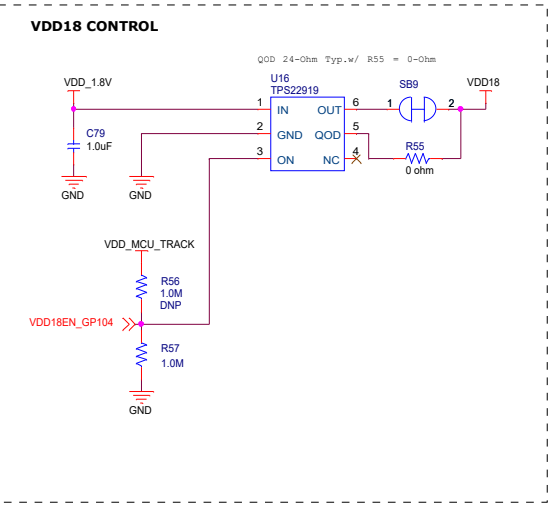
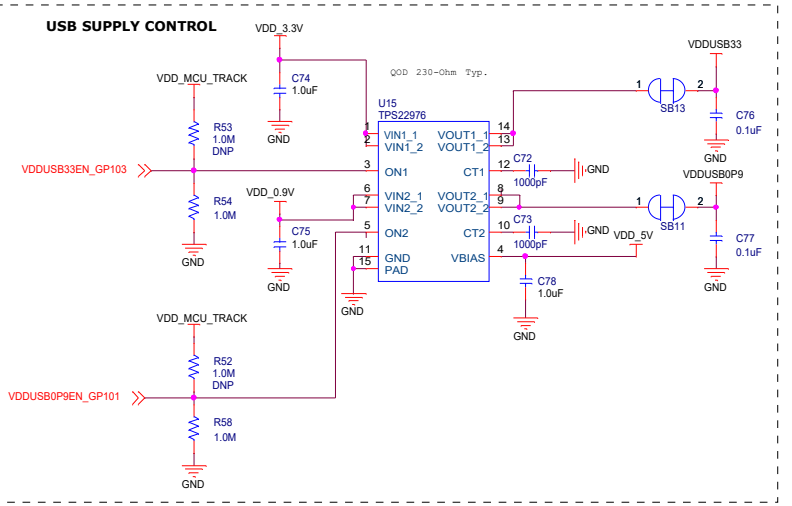
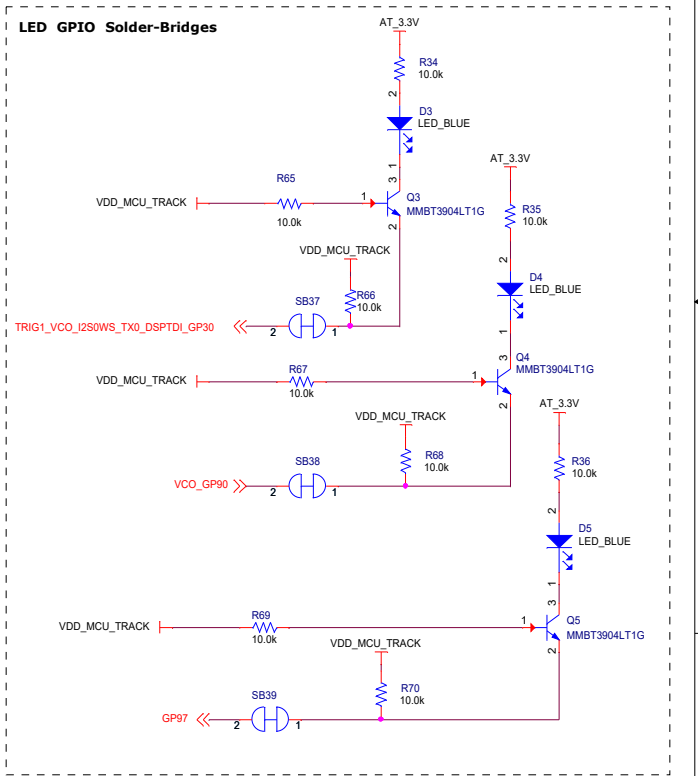
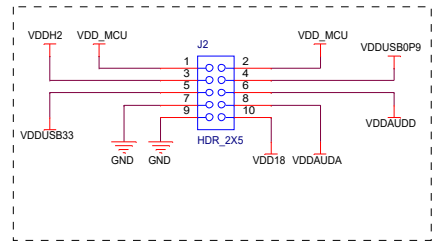
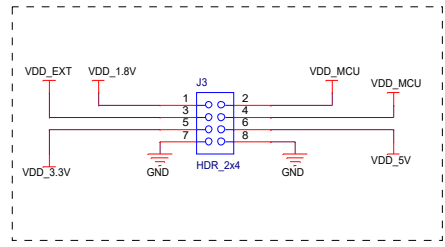
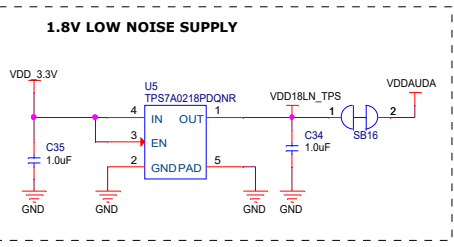
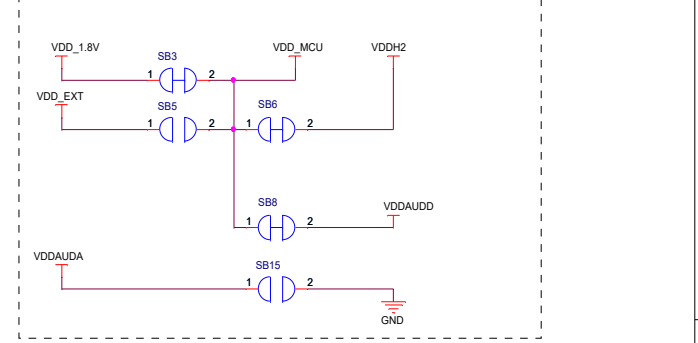
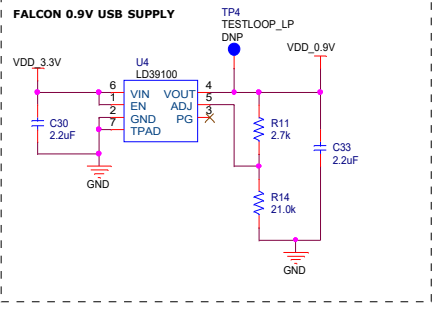
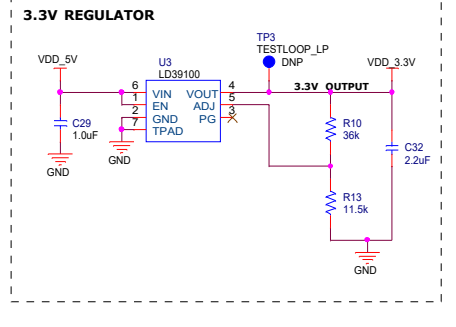
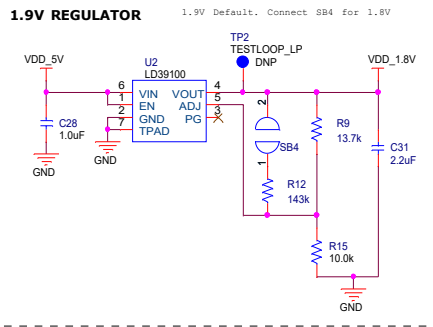
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P03: Power Supplies

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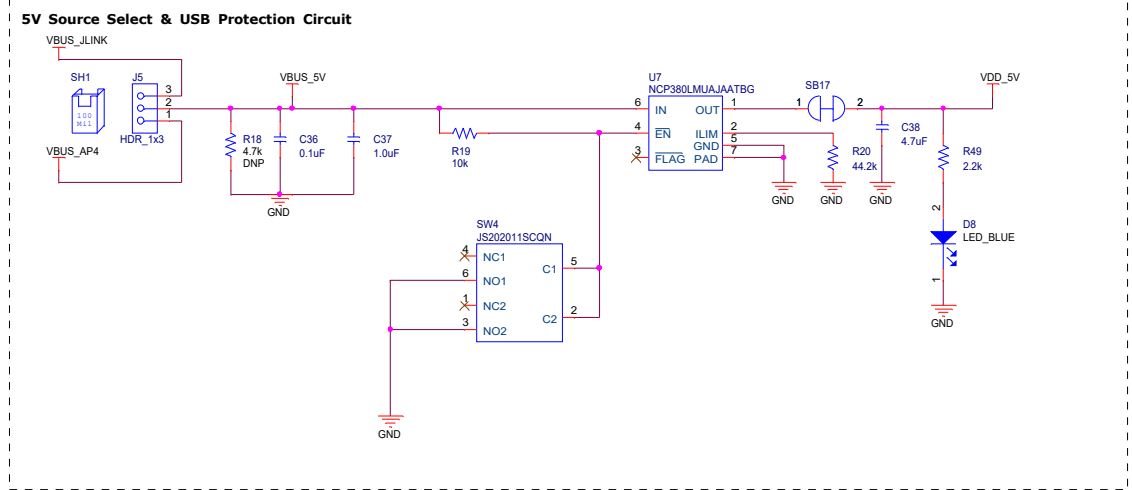
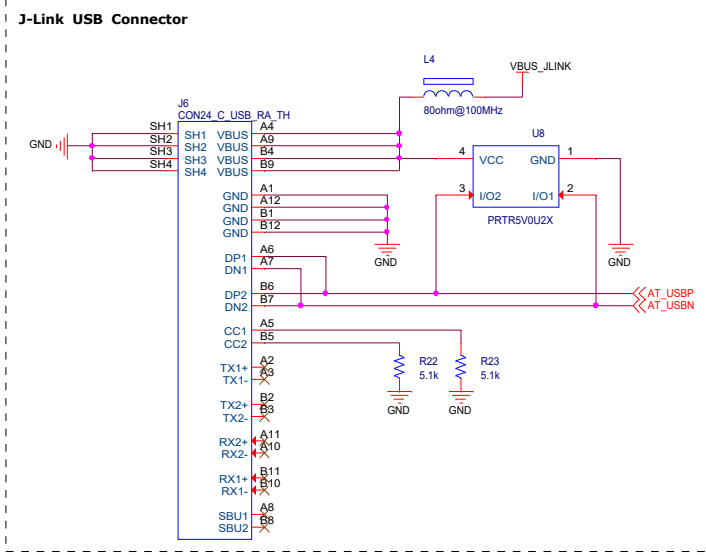
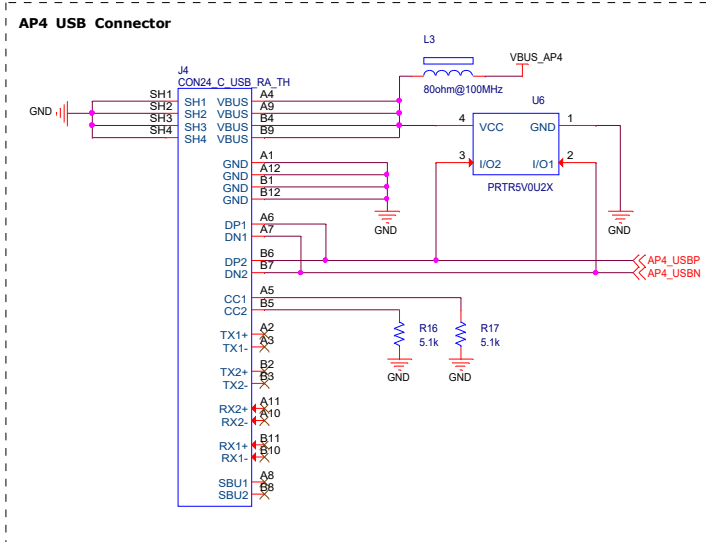
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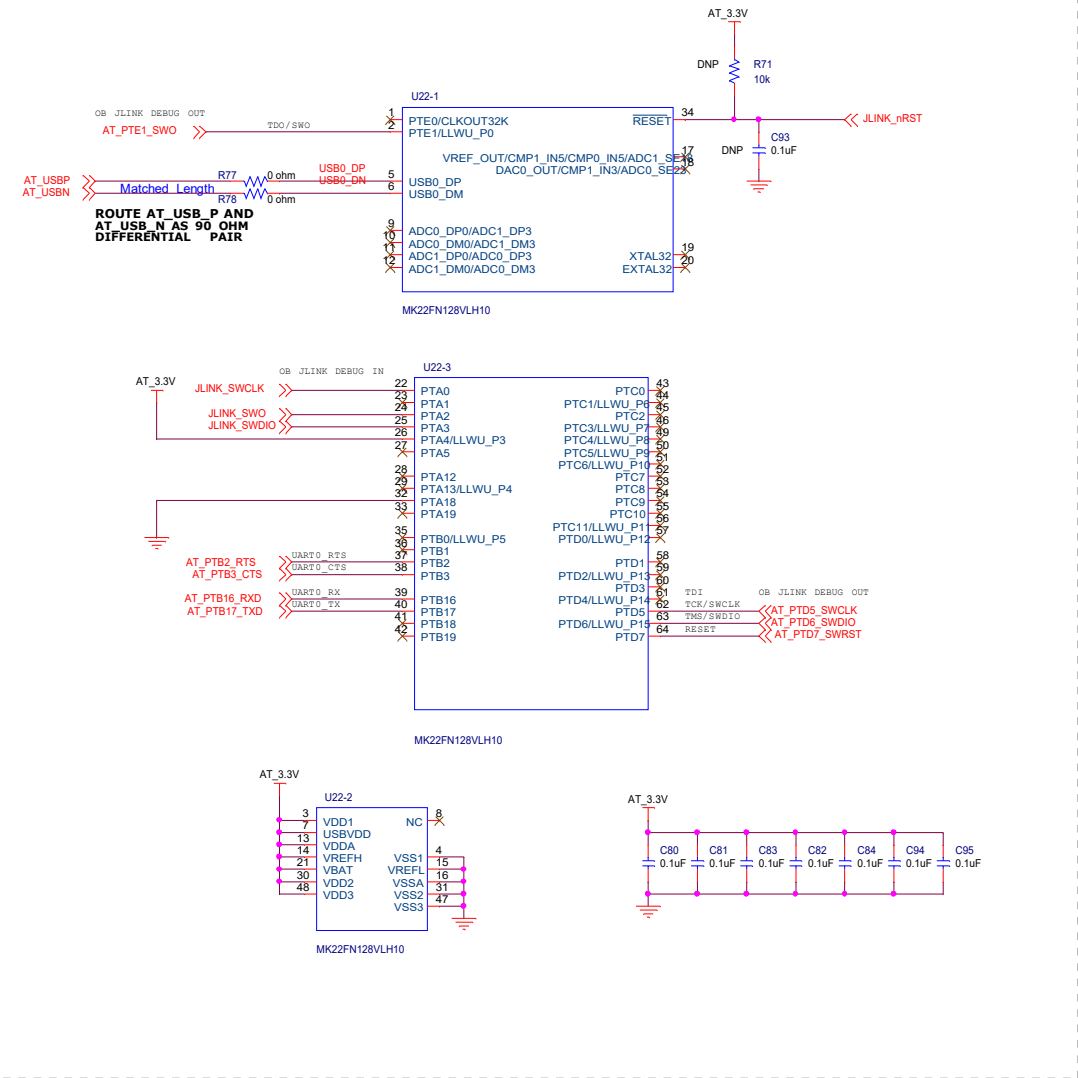
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P04: USB

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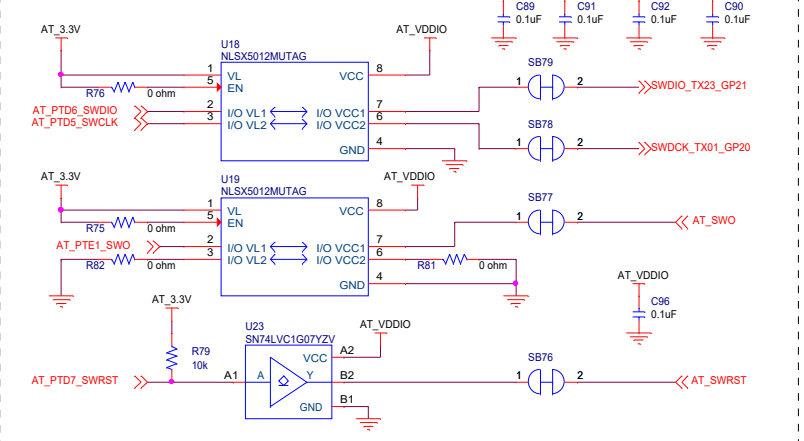
J-Link Onboard Debugger



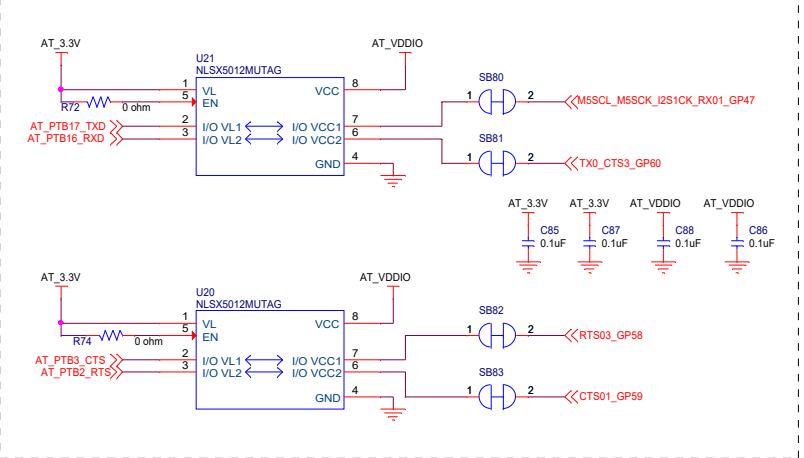
J-Link Voltage Disconnect Solder-Bridges



J-Link SWD Connections and Level Translation



J-Link To AP4 UART Solder-Bridges and Level Translation



P05: Debugger

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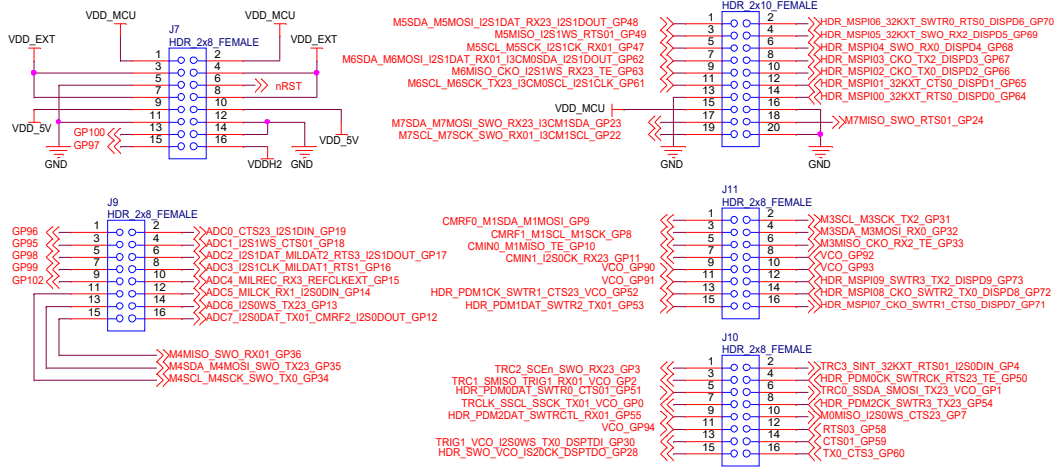
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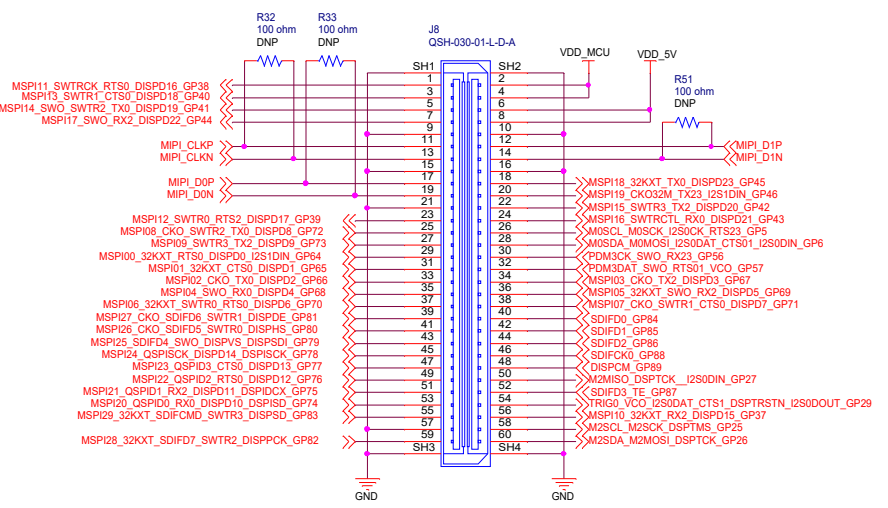
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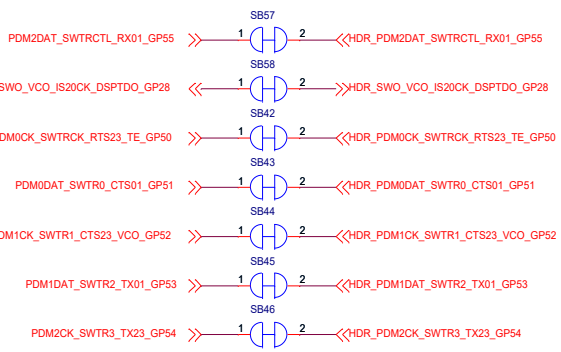
Arduino-Style GPIO Headers



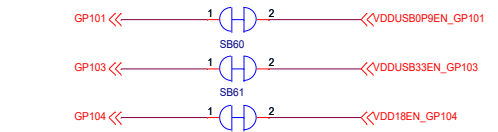
High-Speed Header



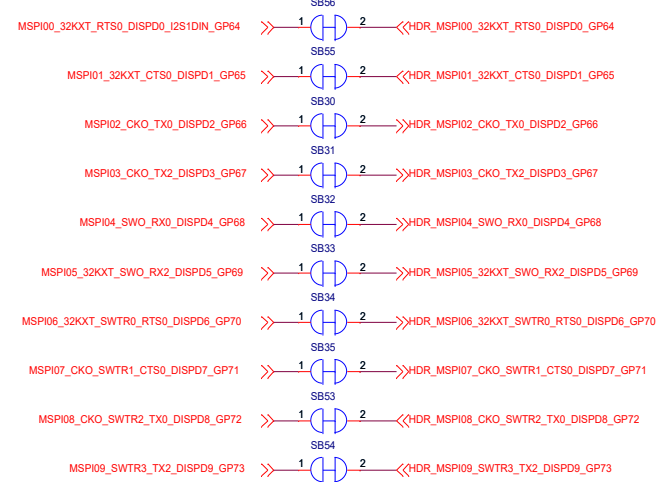
Debug Signal to GPIO Connector Solder-Bridges



USB/MIPI Sequence Control



Display Controller GPIO Solder-Bridges



P06: GPIO Headers

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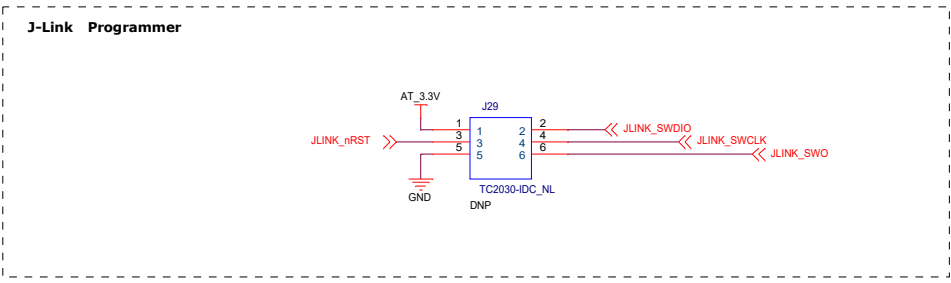
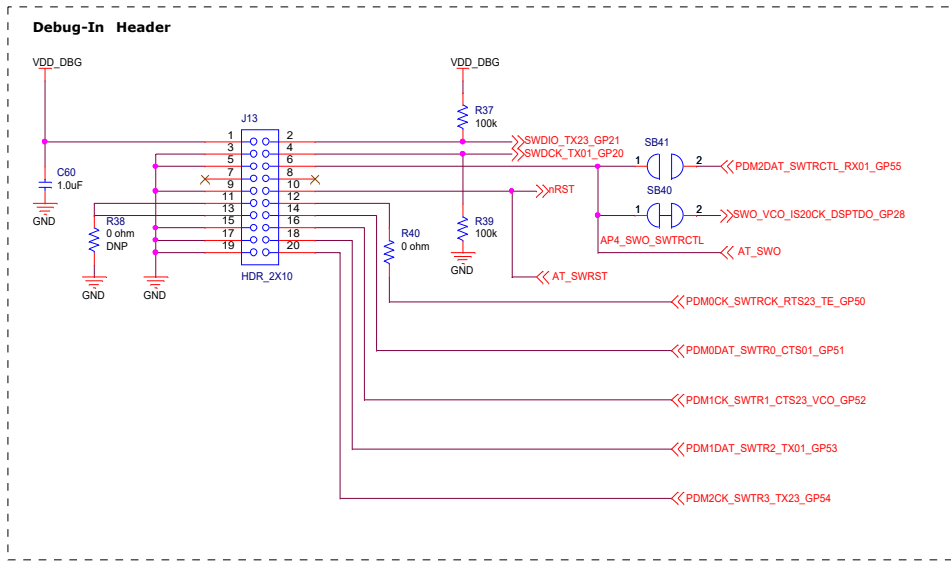
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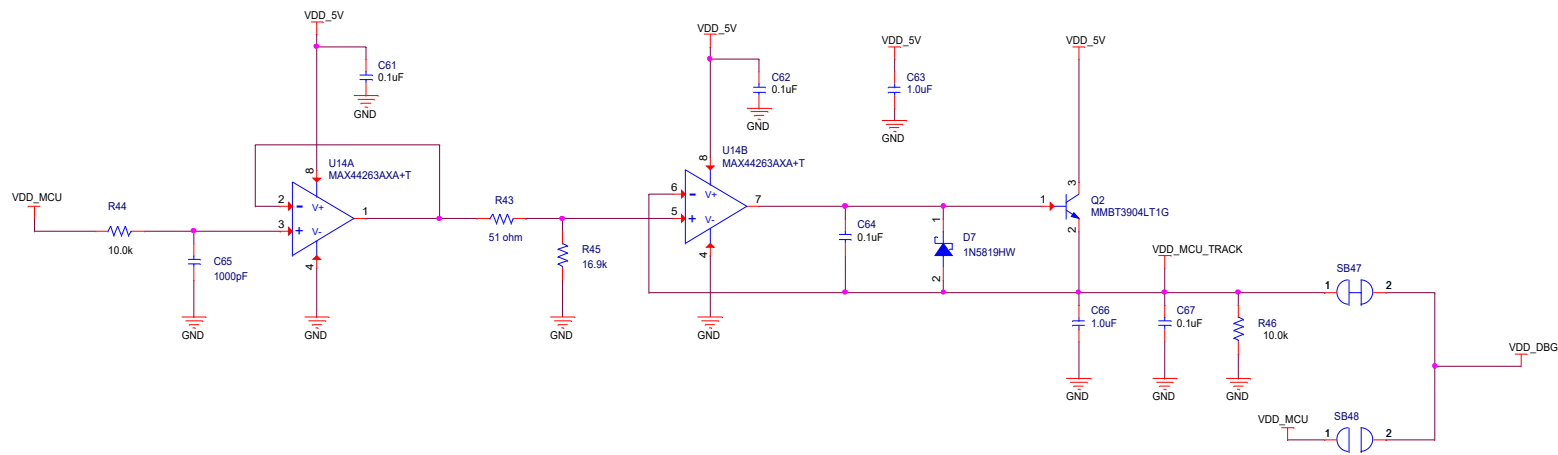
P07: Debug Headers and Support

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P08: Power Supply Tracker

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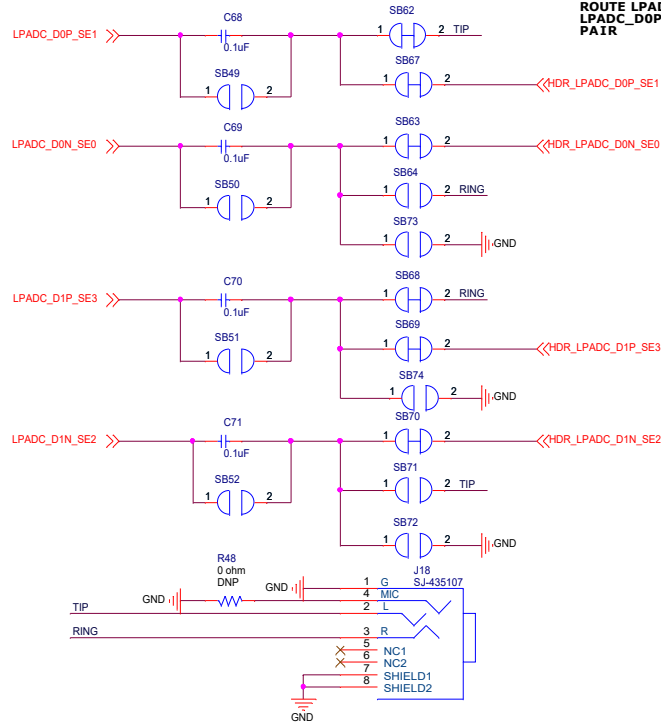


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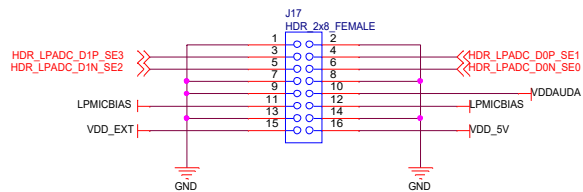
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LPADC 3.5MM Jack



ROUTE LPADC_D1N_SE2 AND LPADC_D1P_SE3 AS DIFFERENTIAL PAIR
ROUTE LPADC_D0N_SE0 AND LPADC_D0P_SE1 AS DIFFERENTIAL PAIR

LPADC Voltage Header



P09: Audio Connections

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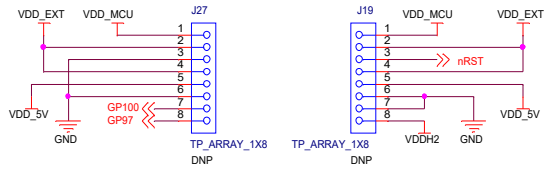
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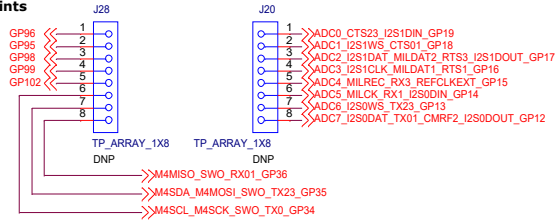
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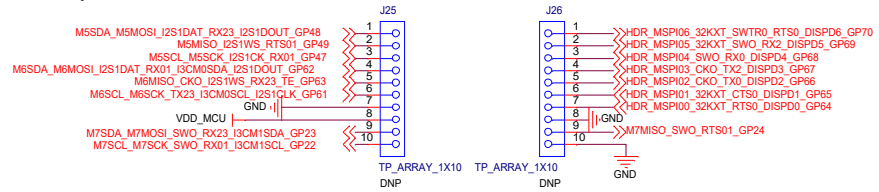
J7 Testpoints



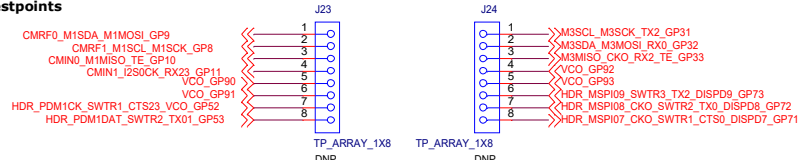
J9 Testpoints



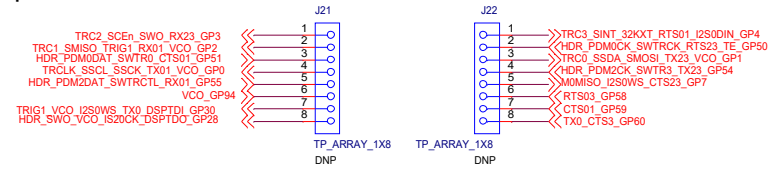
J12 Testpoints



J11 Testpoints



J10 Testpoints



P10: Testpoint Arrays

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